

Amendments to the Specification:

Please replace paragraph [0011] with the following amended paragraph:

[0011] According to an exemplary embodiment of the present invention ~~as set forth in claim 2~~, the ripple suppression circuit comprises a linearly controlled voltage controller, which allows for output voltage variation. Advantageously, such linear control guarantees a supply voltage with minimal ripple and therefore for a output signal with reduced distortion. Advantageously, through the linearly controlled voltage controller, a variable voltage supply (VVs) may be realized without additional costs. The variable voltage supply may allow for drastic reductions of idle losses in the switching amplifier. Furthermore, advantageously, the electromagnetic interference (EMI) during normal operations may be reduced.

Please replace paragraph [0012] with the following amended paragraph:

[0012] Another exemplary embodiment of the present invention ~~as set forth in claim 3~~ provides for a digital amplifier where a coarse supply voltage setting is applied in front of the controlled voltage controller. Due to the only coarse supply voltage regulation, cheap and even imprecise power supplies can be used in combination with the digital amplifier while achieving reduced distortions at the amplifier output. Thus, for example a battery may be used as the power supply.

Please replace paragraph [0013] with the following amended paragraph:

[0013] Another exemplary embodiment of the present invention ~~as set forth in claim 4~~ provides for a digital amplifier wherein the ripple suppression circuit is configured to drive one transistor of a pair of transistors of for example a bridge configuration of a class D amplifier in the linear region of this transistor. Advantageously according to this exemplary embodiment of the present invention, voltage drops inside the power stage are compensated by taking into account all possible voltage drops including losses in copper

tracks and semiconductor packages. Advantageously, according to this exemplary embodiment of the present invention, voltage drops due to a weak power supply control or small output capacitance will be compensated as well. Furthermore, advantageously, due to a reduction of power supply requirements with regard to output voltage stability, a significant cost reduction opportunity is achieved.

Please replace paragraph [0014] with the following amended paragraph:

[0014] ~~Further Claims 5, 6 and 7 provide for further~~ exemplary embodiments of the present invention which advantageously allow to reduce distortions in the output signal of the digital amplifier by reducing or fully compensating ripples in the power supply voltage of the digital amplifier while keeping costs low.

Please replace paragraph [0015] with the following amended paragraph:

[0015] According to another exemplary embodiment of the present invention ~~as set forth in claim 8~~, the digital amplifier system according to the present invention is integrated into a module or an integrated circuit. This advantageously allows to provide for a digital amplifier with minimal dimensions while having reduced distortions. Furthermore, advantageously, this provides for the amplifier stage and the compensation circuit on one chip.

Please replace paragraph [0016] with the following amended paragraph:

[0016] According to another exemplary embodiment of the present invention ~~as set forth in claim 10~~, the digital amplifier is a class D amplifier, preferably with a H-bridge and the compensation margin of the ripple suppression circuit is adapted to the output power of the amplifier.

Please replace paragraph [0017] with the following amended paragraph:

[0017] Another exemplary embodiment of the present invention ~~is set forth in claim 10,~~
~~and~~ provides for a ripple suppression circuit for connection between a power supply and a class D amplifier, comprising a linearly controlled voltage controller, which may be connected between a power supply source and a H-bridge of a class D amplifier. Advantageously, while reducing requirements for the voltage stability of the output voltage of the power supply, which reduces the costs of the power supply, the ripple suppression circuit according to this exemplary embodiment of the present invention allows for a reduction of distortions in the output signal of a class D amplifier.

Please replace paragraph [0018] with the following amended paragraph:

[0018] Another exemplary embodiment of the present invention ~~as set forth in claim 11~~
provides for a ripple suppression circuit where one transistor of at least a pair of transistors of a class D amplifier is operated in a linear region of this transistor, allowing for reduced distortions in the output signal of the amplifier.

Please replace paragraph [0019] with the following amended paragraph:

[0019] Exemplary Claims 12 and 13 ~~provide for exemplary~~ embodiments of a method for suppressing ripples in a supply power of class D amplifiers, ~~which~~ allow an operation of the class D amplifiers with reduced distortions.